

60

4

“ ”

1

2

3

4

1.

1

2

3

3

2.

1

2

3

4

5 555

6 A/D D/A

3.

1

2

3

4 555

5

"

"

1	1		12	
2	2		12	
3	3		12	
4	4		12	
5	5		12	
			60	

1 1

1

2

3

4

2 2

1

2

3

3 3

1

2

3

4

2

3

The diagram illustrates a signal processing system with four parallel paths, labeled 1, 2, 3, and 4. Each path consists of a D/A converter followed by an A/D converter. The paths are arranged vertically, with path 1 at the top and path 4 at the bottom.

- Path 1: D/A → A/D
- Path 2: D/A → A/D
- Path 3: D/A → A/D
- Path 4: D/A → A/D

u *u* *u*

t

1
2
3
4

20%

20% 30% 30%

1 20%
1 30%

2 30%

2 30%

u *u* *u*

+

+

u

1 30%

2 + 20%

1

2

" "

3

4

1

2

2021 7 10